Join Intel for a Technical Talk
Tapeout Technology Development is hiring!
Presented by Ravi Vyas

(intel) Look Inside.™

Date: Thursday, Oct 29th 2015
Time: 5:30 – 7:00 p.m.
Location: Seeley G. Mudd (SGM) 101

Immediate opportunities for MS and PhD students.

Intel is seeking candidates for **Software Engineering positions**:

Electrical Engineering	Computer Science	Computer Engineering
Physics	Chemistry	Chemical Engineering

BRING YOUR RESUME!!!

Join Ravi Vyas from Tapeout Technology Development (and a USC alumnus) for a discussion on Intel's Silicon process technology and Tapeout Automation. Intel has made significant advancement in Silicon process technology in recent years and brought advanced technologies to High Volume Manufacturing years ahead of its competitors. As we look to the future, Intel will continue its investment in inventing and developing new technologies.

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